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Rapid prototyping for multi-application sensor networking

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Developing integrated hardware interfaces for different actuators allows rapid and easy integration into existing wireless networks.

Wireless sensor networks (WSNs) have become widely relevant in recent years, facilitating data collection through a large number of nodes measuring different physical parameters in various environments. Such networks require low power consumption in the individual sensors and adequate protocols for communication between the nodes. Effective platform hardware design is also crucial. Key features of the technology are the ubiquity of these systems, the need for transparency to the end user, and the variety of predicted applications. Because so many possible applications exist, using WSNs to their fullest potential depends on our ability to adapt new applications to existing platforms.

Rapid prototyping can assist in the process of adapting applications to current platforms. However, one of the main obstacles to this approach is that sensors have heterogeneous interfaces. Some of the numerous different transducer (sensor/actuator) interfaces currently in existence, such as the inter-integrated circuit (I2C), are very popular. However, there will probably never be a single common interface for all types of transducer because of commercial interests and special features. Therefore, when adapting a new sensor, most of the work must be done from scratch, making it difficult to develop applications quickly. This is the problem we are trying to solve.

Here we propose a very high speed integrated circuit (VH-SIC) hardware description language (VHDL) library for sensor and actuator interfaces. The purpose is to have a set of interfaces that accommodate the most common sensors and actuators, enabling rapid connections to new equipment. Moreover, the concept presented here may be used for new interfaces that can be easily developed. The VHDL implementation is independent of the final platform; any field-programmable gate array (FPGA) or application-specific integrated circuit may be used. This min-

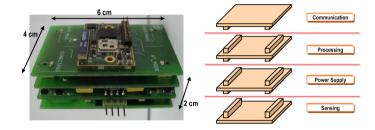


Figure 1. The four-layer architecture for sensor nodes in WSN contains layers for communication, processing, power supply, and sensing/actuating.

imizes redesign effort and makes rapid prototyping easier. The interfaces are installed in a hardware platform for sensor networks developed at our institution.

Hardware node design becomes critical in WSNs for meeting the requirements mentioned above. Modular hardware nodes are often used to make rapid prototyping easier, as well as to allow for quicker redesign and the ability to reuse some of the hardware modules. ^{1–3} With this approach, it is easy to redesign the platform to adapt the system to different scenarios and applications. This concept was developed in our research group's previous work, and our modular platform is available for research and development. ⁴ Figure 1 shows the four-layer platform, the main features of which are modularity, low cost, medium size, and easy adaptation to different applications.

This modular platform is divided into four functional layers: communication, processing, power supply, and sensing/actuating. The processing layer includes a microcontroller and an FPGA: this gives the platform a great deal of processing power as well as flexibility. Modularity in the hardware node must be combined with flexibility in the processing devices in order to obtain the maximum adaptability.

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To simplify the connection and use of transducers, we aim to create a library with the most common interfaces for sensors, and to improve and extend this library as new transducers come on the market. Moreover, our strategy is to develop the interfaces in hardware to exploit the concurrency possibilities of the processing layer of the platform, allowing the microcontroller to perform other tasks related to the control of the global system. Figure 2 shows a diagram of the implementation strategy used for the interfaces, using our platform as the base for the WSN.

The interfaces are described in VHDL, which makes the solution independent of the final implementation as long as it is based on custom hardware. The library is composed of different modules that deal directly with every transducer (analog or digital) but work through a common interface. The transducer is connected to the corresponding module of the library, which is different for every interface, and finally this module is connected through the common interface to the rest of the circuit (see Figure 3). This type of architecture is based on the IEEE-1451.2 standard.⁵

In particular, the different modules developed in the proposed library are used for pulse-width modulation, frequency/period modulation, I2C, Sensirion interface (similar to I2C), interface for analog transducers (module to interact with analog-to-digital

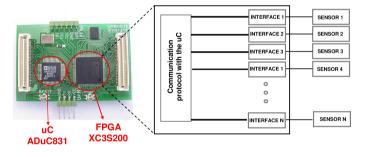


Figure 2. Interfaces for transducer control adapted to WSN platform.

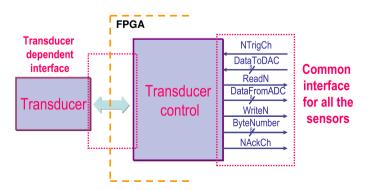


Figure 3. General structure chosen for the control of a transducer.

and digital-to-analog converters), and 1-Wire $^{\circledR}$. Detailed information about the interfaces is available in our paper.⁶

This library is used on the modular platform for WSNs, and makes the sensor almost transparent for the user, who always sees the same signals independently of the sensor being used. Exhaustive simulations have been carried out and experimental tests have also been conducted for every module in the library. The transducer interface modules, integrated into our platform, are currently being used for several applications under development in areas including the structural control of walls, the mining industry, and fruit transportation control. In addition, an entertainment application—which includes a handheld device that can be manipulated to produce musical effects—is under development. This is demonstrated in our video clip. (Make sure to switch your loudspeakers on!)

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- 7. Video illustrates an entertainment application with a handheld device that produces musical effects. *Credit: Teresa Riesgo, Polytechnic University of Madrid.* http://spie.org/documents/newsroom/videos/0851/CookiesApp.avi